

EAST - [Default EAST Workspace 1600x1200.wsp:1]

FileViewEditToolsWindowHelp

Active

L1: (194131) processors or process\$3 adj (elements or circuits)

L2: (3427812) image\$1 or pixel\$1 or transform\$5

L3: (35241) (2 or video) same 1

L4: (437) cosine same 3

L5: (99157) output adj (register or buffer or storage or memory)

L6: (31278) 5 adj10 output

L7: (0) 6 adj (register or buffer or storage or memory)

L8: (56) 4 and 6

L9: (244) transform\$5 same 3 and 6 not 8

L10: (971) 708/40? .ccls.

L11: (36) 9 and 10

L12: (4130) array near5 1

L13: (41) 10 and 12

L14: (33) 13 not (8 or 11)

L15: (356) 1 and 10

L16: (87) Forward citation search 1

L17: (9060) frame adj buffer

L18: (338551) saturat\$3

L19: (59) 17 same 18

L20: (12) 3 and 19

L21: (1979) idct or inverse adj cosine

L22: (0) 19 and 21

L23: (65) 17 and 18 and 21

L24: (252) 708/401,402.ccls.

L25: (15) 18 and 24

L26: (1073) dct with idct

L27: (9) 25 and 26

Failed

Search

DBsUS-PGPUB:USPAT:USOCR:EPO:JPO:DERWENT:BM:YDB

Plurals

Default operatorADJ

Highlight all hit terms initially

BRS formIS&R formImageTextHTML

	U	I	Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Retrie	Inventor	S	C	P	3	In
1	✓	✓	US 3754128 A	19730821	27	HIGH SPEED SIGNAL PROCE	708/400	708/403		Corinthios, Michael J. G.	✓	✓	✓	✓	U:
2	✓	✓	US 4791598 A	19881213	14	Two-dimensional discrete cosin	708/402	708/401		Liou, Ming-Lei et al.	✓	✓	✓	✓	U:
3	✓	✓	US 4821224 A	19890411	14	Method and apparatus for proc	708/401	708/409		Liu, Wentai et al.	✓	✓	✓	✓	U:
4	✓	✓	US 5361370 A	19941101	25	Single-instruction multiple-data	712/22	712/11		Sprague, David L. et al.	✓	✓	✓	✓	U:
5	✓	✓	US 5430854 A	19950704	27	Simd with selective idling of indi	712/236			Sprague, David L. et al.	✓	✓	✓	✓	U:
6	✓	✓	US 6304887 B1	20011016	14	FFT-based parallel system for a	708/404			Ju, Chwen-Jye et al.	✓	✓	✓	✓	U:
7	✓	✓	US 6430589 B1	20020806	25	Single precision array processo	708/620	708/523		Jennings, III; Earle W.	✓	✓	✓	✓	U:

HitDetailsHTML

Ready

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